

FIG. 1

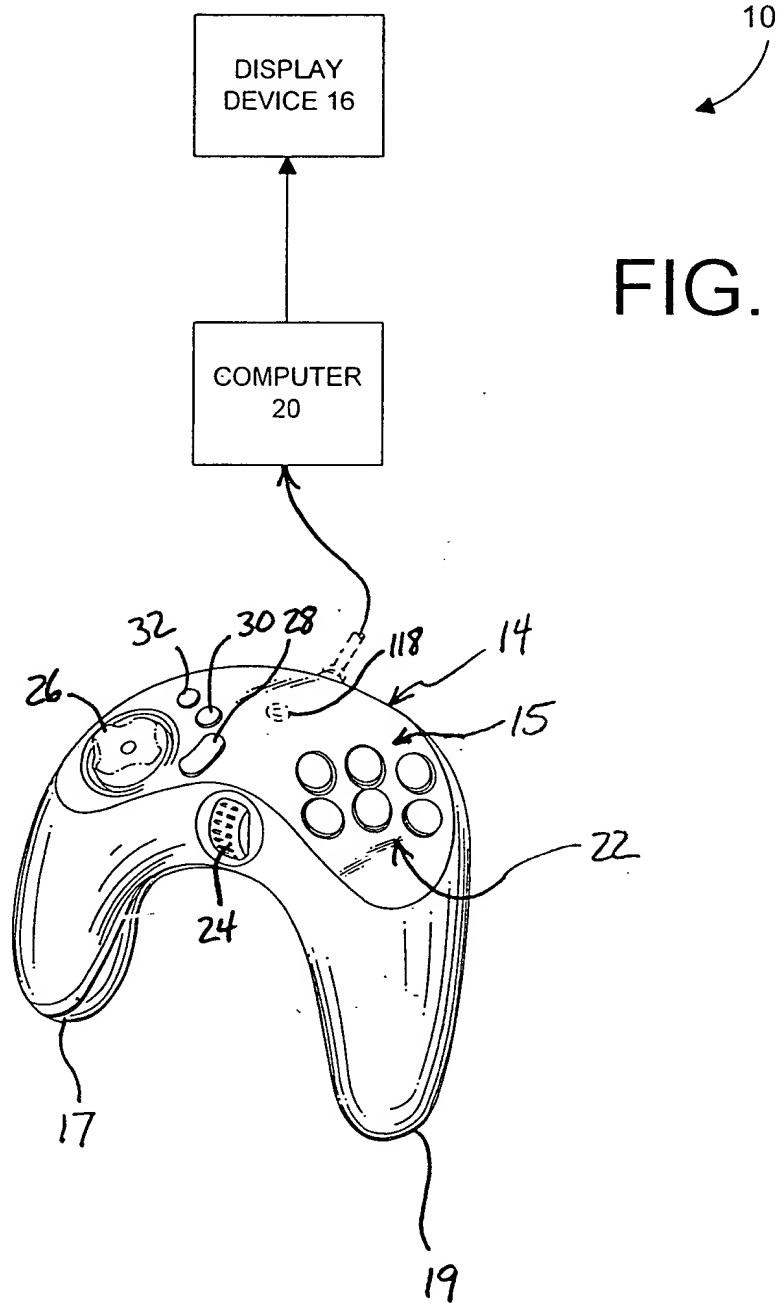
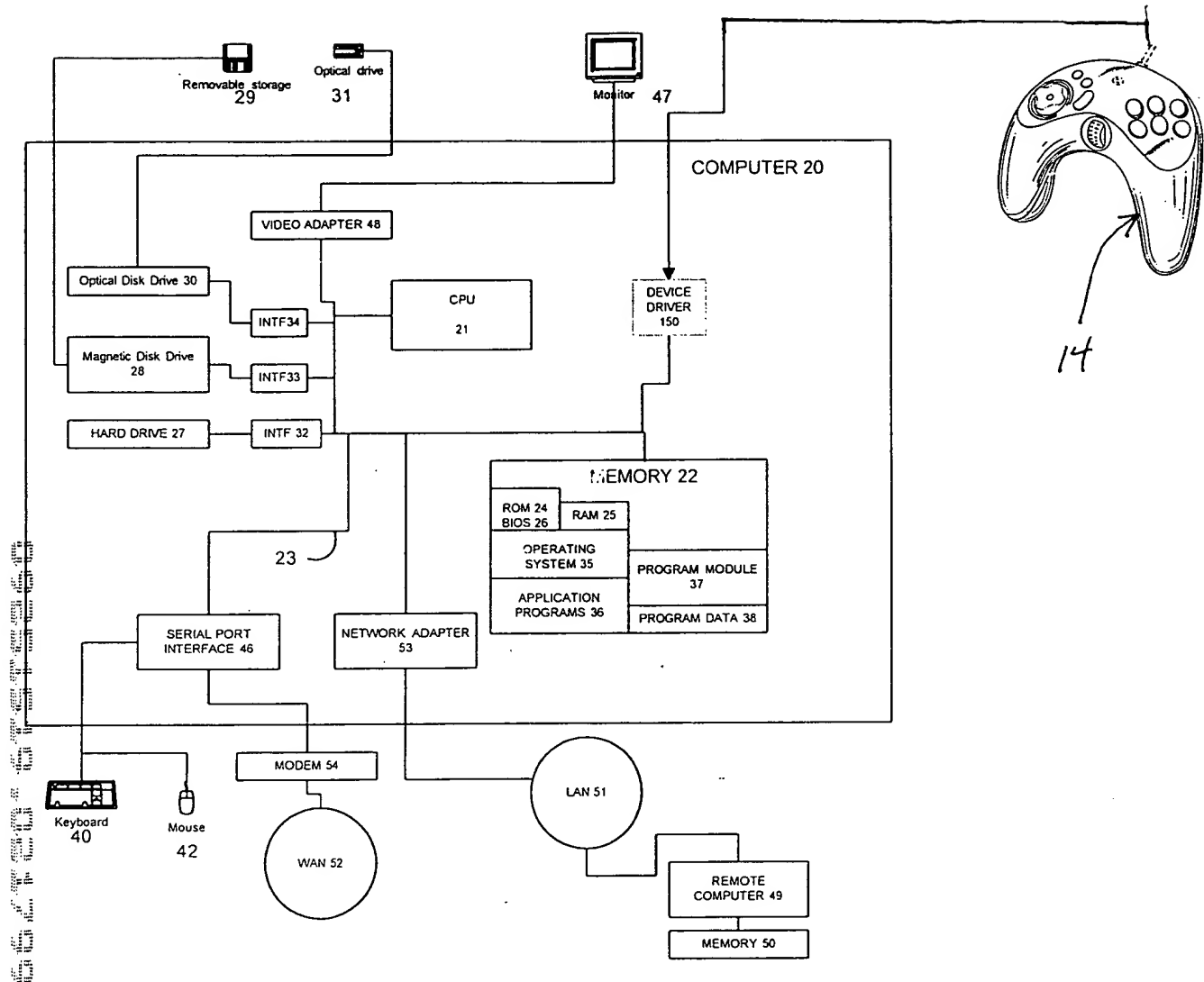


FIG. 2



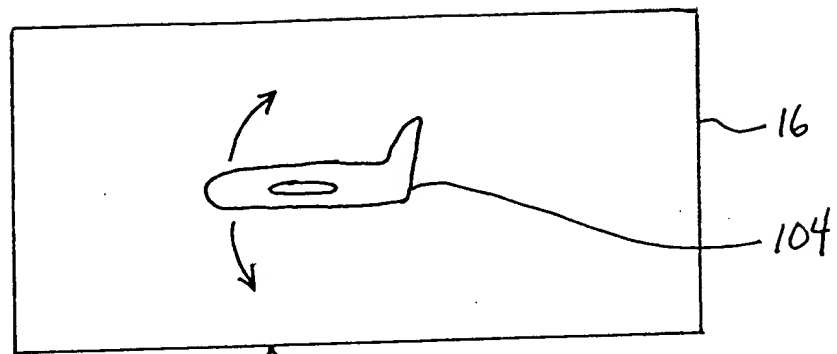
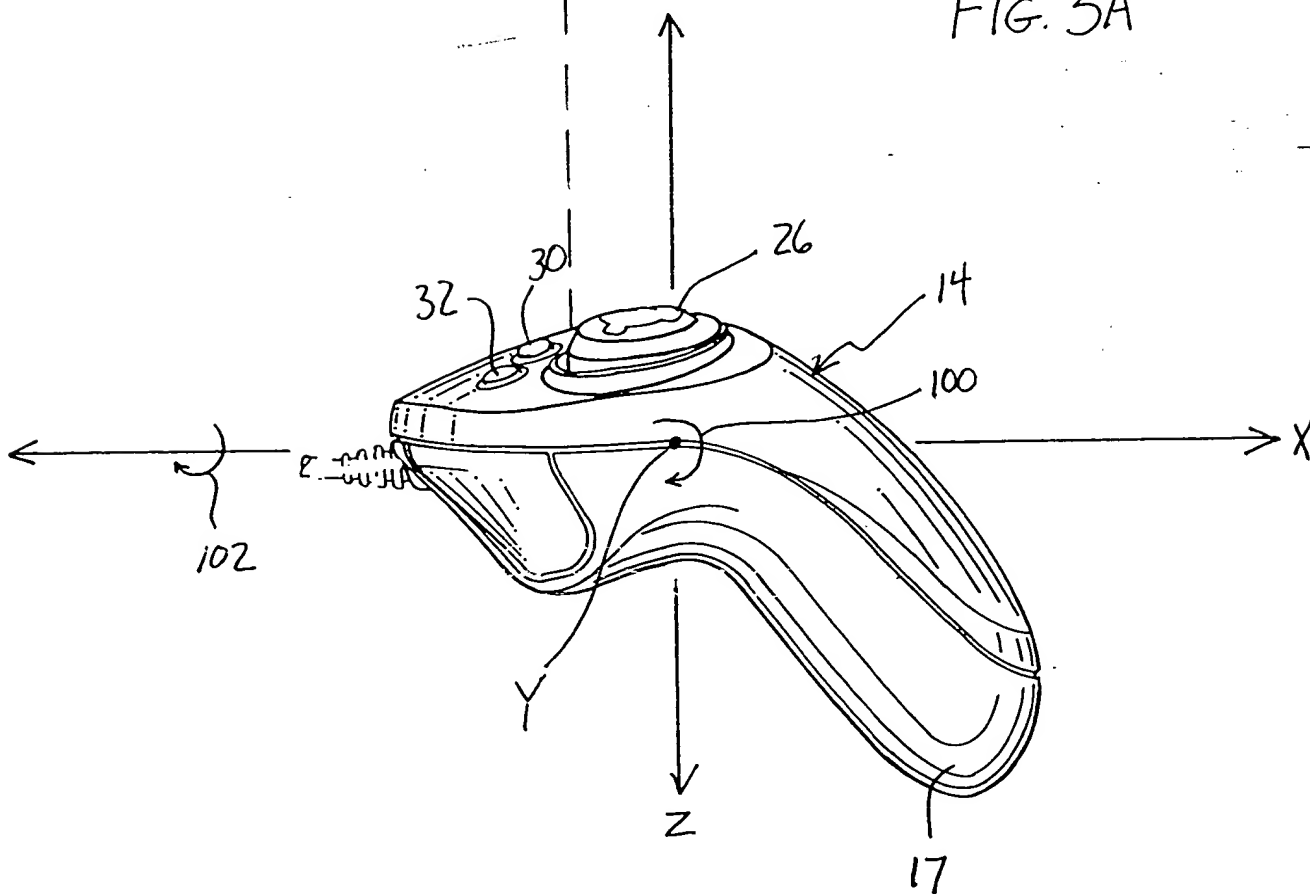


FIG. 3A



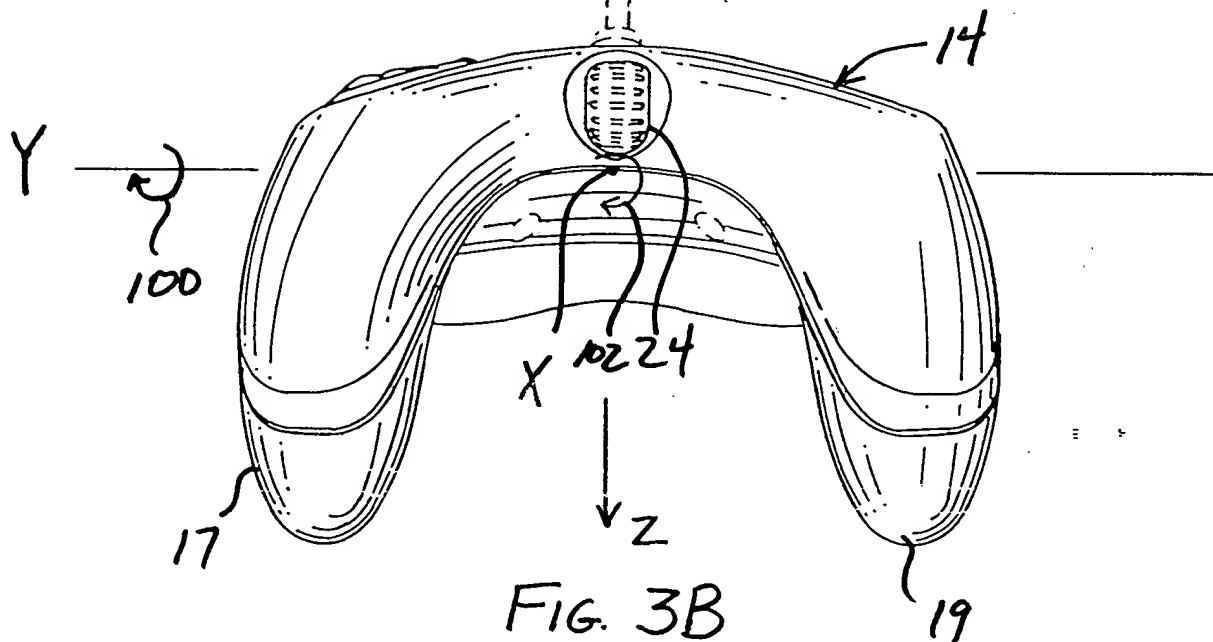
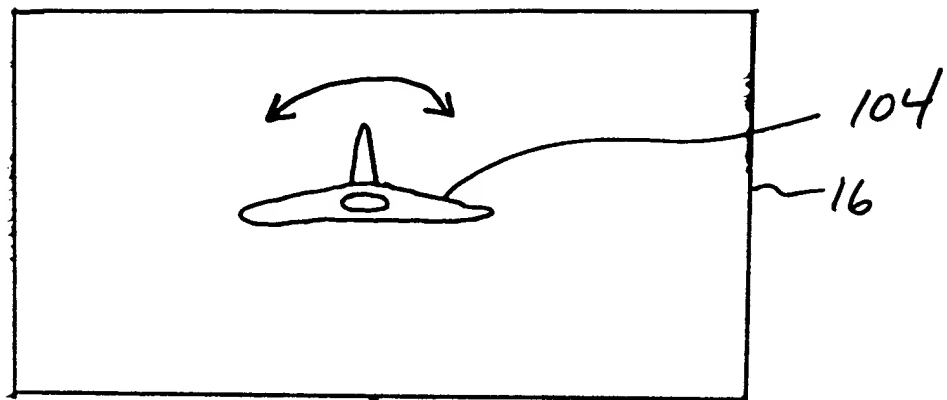


FIG. 3B

	BIT							
	0	1	2	3	4	5	6	7
BYTE 0	X0	X1	X2	X3	X4	X5	X6	X7
BYTE 1	X8	X9	Y0	Y1	Y2	Y3	Y4	Y5
BYTE 2	Y6	Y7	Y8	Y9	W0	W1	W2	W3
BYTE 3	W4	W5	A0	A1	A2	A3	A4	A5
BYTE 4	T0	T1	S	SH	B	F0	F1	D0
BYTE 5	D1	D2	D3	SP	SP	SP	SP	SP

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FIG. 5

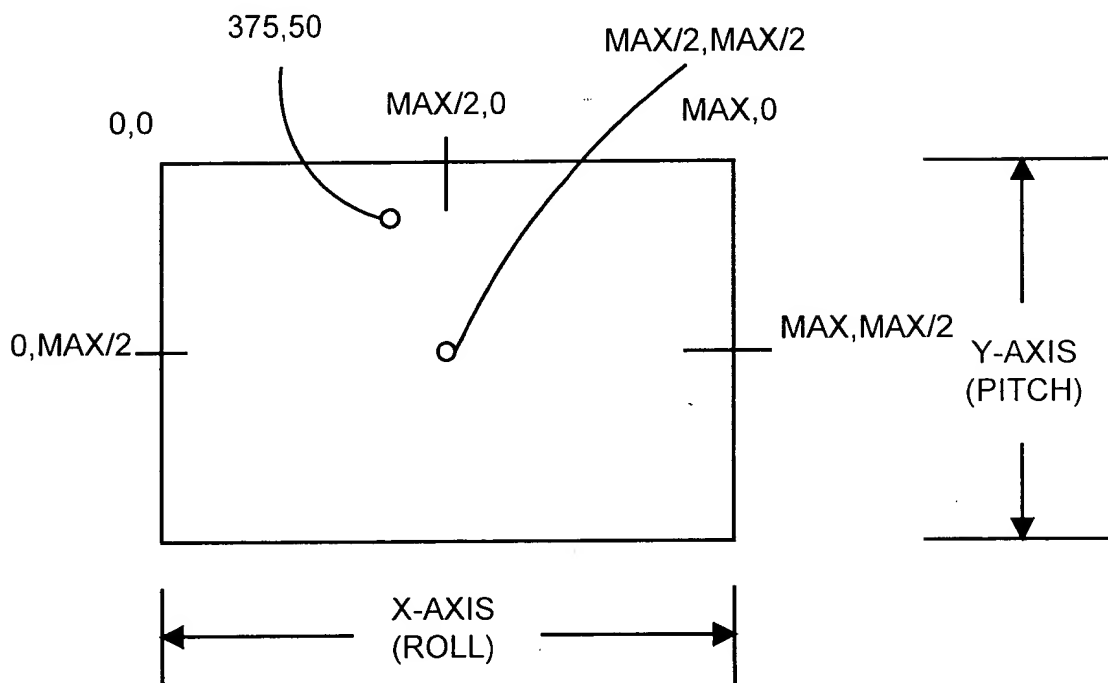


FIG. 3C

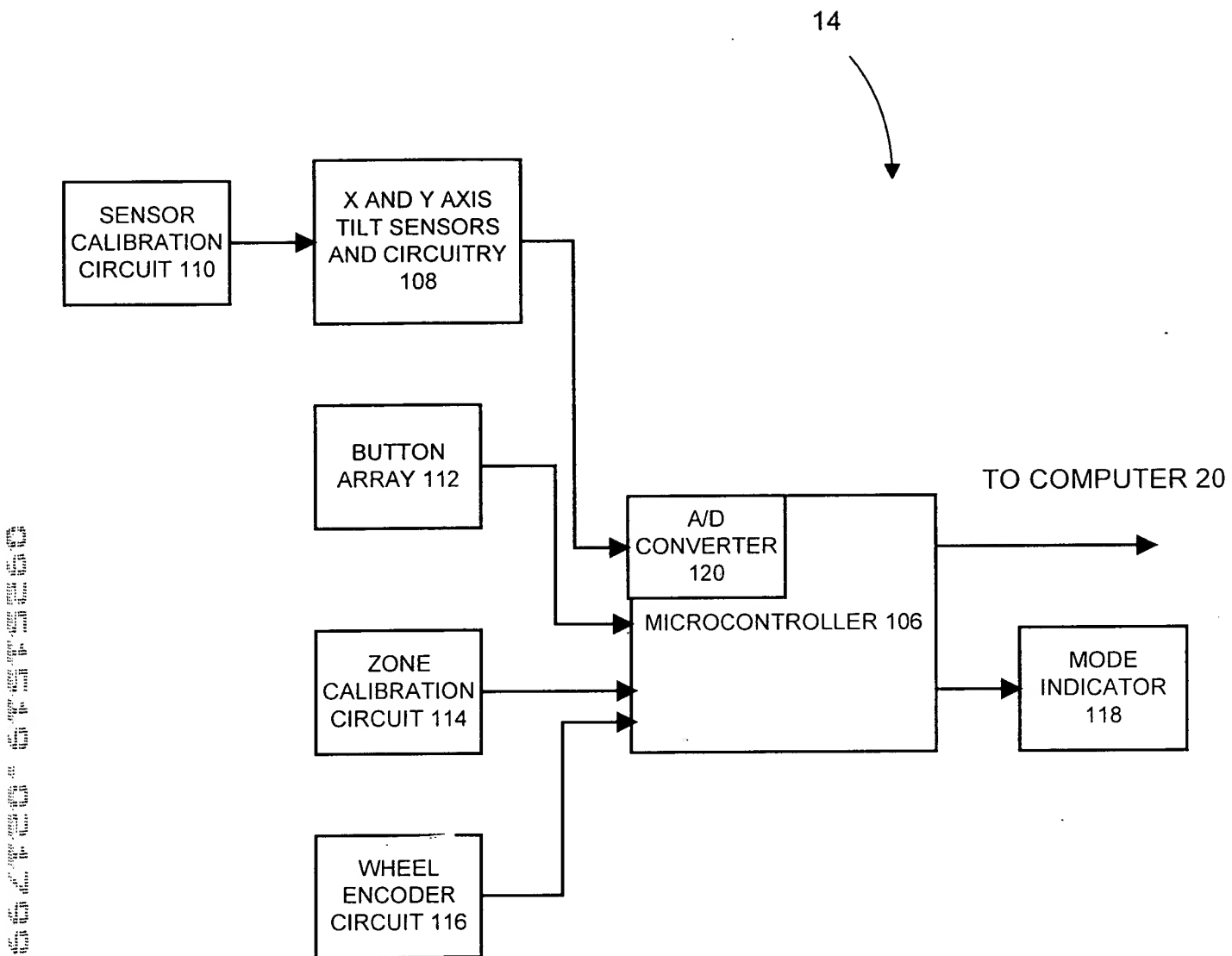
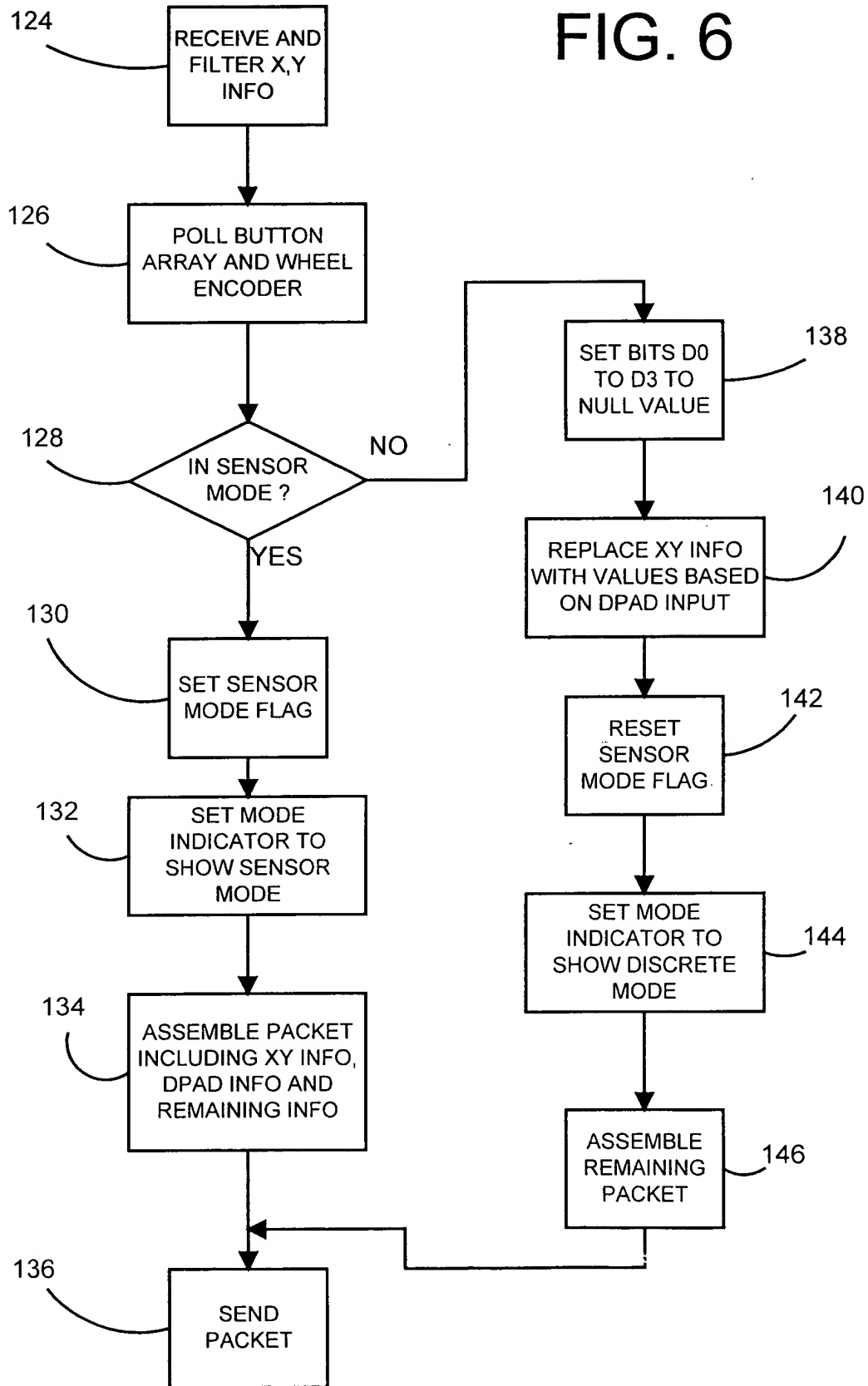


FIG. 4

FIG. 6



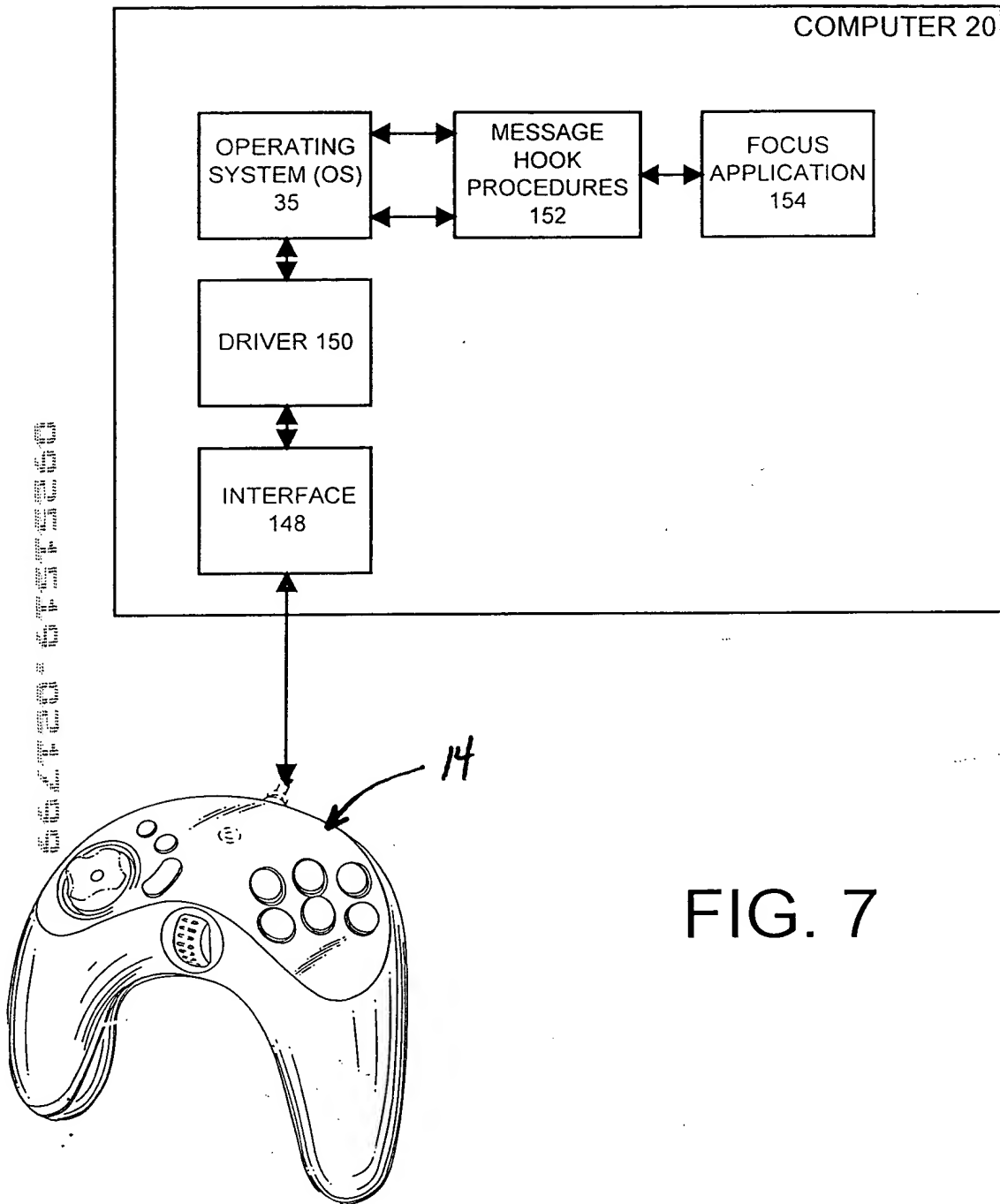
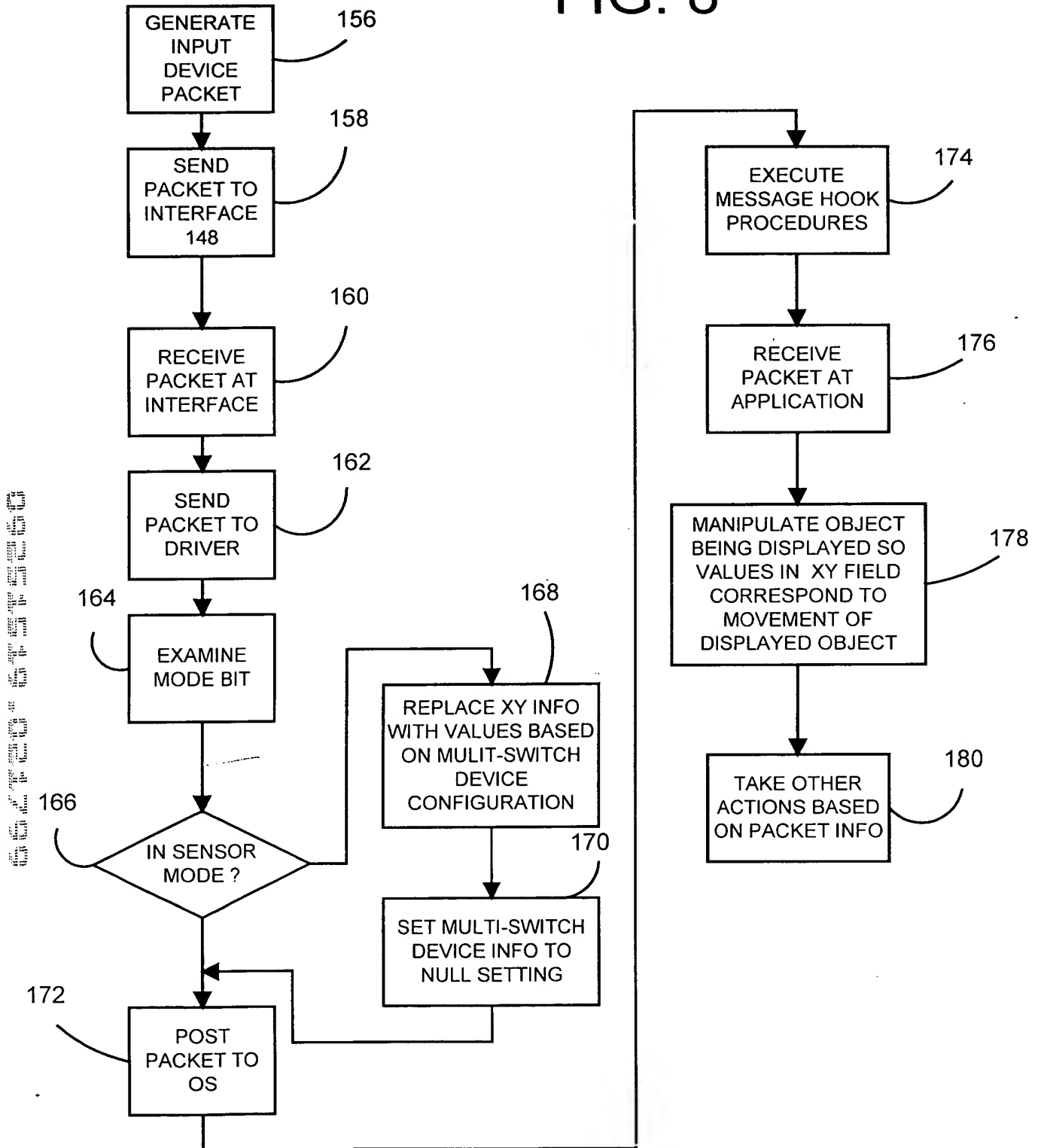
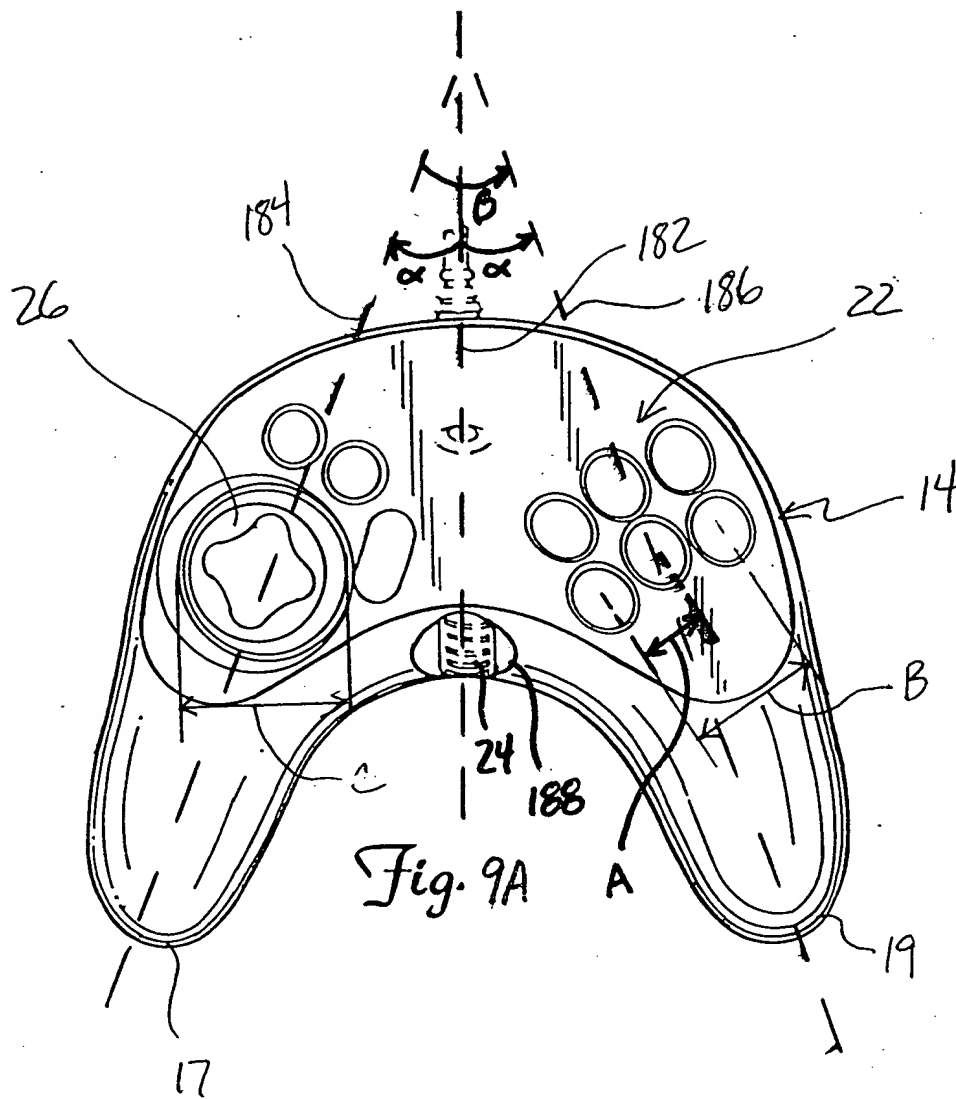


FIG. 7



FIG. 8





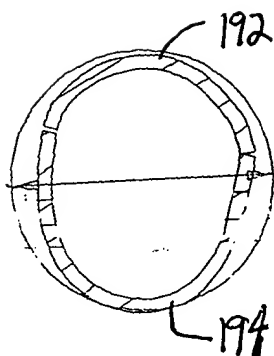
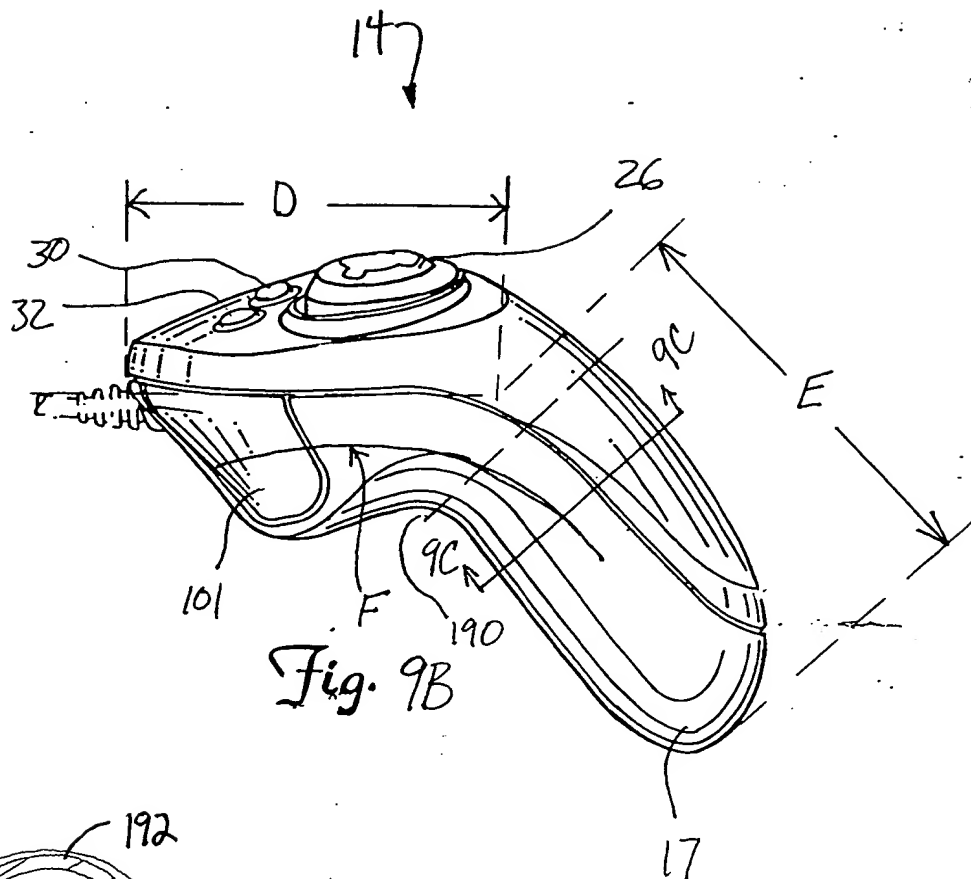


FIG. 9C